Introduction to the signal clock calculus

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Definition (Signal)

Let *D* be some domain. A signal *X* is a sequence $X = (x_i)_{i \in I}$ of values $x_i \in D$.

A signal X is

- present at instant *t*, if *X* carries a value at that instant.
- absent at instant t, if X carries no value at that instant (write: X = ⊥).



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Example

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As an example, three signals X, Y, Z are given by the table below.

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	t_1	t ₂	t ₃	t ₄	t_5	t ₆	t7	t ₈			
Ζ	false	true	\perp	true	false	\perp	false	true			
Y	false	true	\perp	\perp	true	\perp	false	true			
Χ	true	false	\perp	true	true	\perp	true	false			

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Definition (Clock)

Let c be the set of instants, at which the signal X is present. Then c is called the *clock* X, denoted \hat{X} .

Described formally by clock algebra

 $\mathcal{H} = (U, \cup, \cap, \setminus, \mathbb{O})$



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Boolean clocks

For a Boolean signal C, let

- [C] the set of instants at which C is present and *true*
- $[\neg C]$ the set of instants at which C is present and *false*

 \rightsquigarrow It holds that $[C] \cup [\neg C] = \widehat{C}$ and $[C] \cap [\neg C] = \mathbb{O}$.



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Recall the example signals X, Y, Z. Its easy to see that $\widehat{X} = \widehat{Z} \subseteq \widehat{Y}$.

- X true false \perp true true \perp true false
- Y false true \perp \perp true \perp false true
- Z false true \perp true false \perp false true

 t_1 t_2 t_3 t_4 t_5 t_6 t_7 t_8 \cdots

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Functions Let $X := f(X_1, X_2, ..., X_n)$, s.t. • X is present \Leftrightarrow all X_i are present • $x^t = f(x_1^t, x_2^t, ..., x_n^t)$ for all $t \in \widehat{X}$

Delay Let $X := Y \$ n init \overrightarrow{V} , s.t. • X is present \Leftrightarrow Y is present • $x^{t+n} = y^t$ for t > n, v_i otherwise.



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Downsampling Let X := Y when C, s.t.

X is present ⇔ Y present and C = true
x^t = y^t for all t ∈ X

Det. merge Let X := Y default Z, s.t.

• X is present \Leftrightarrow Y or Z are present

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Composition The | operator forces that all its operands equations must hold in parallel



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Overview

- Extract synchronization contraints
- Extract data-dapendency
- Solve clock equations
- Generate control structure



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Motivation

- know if specification is consistent
- Automatic code generation
- Code that respects synchronization
- Efficient code



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Synchronizations





Data-dependency

Process	Dependency				
each signal X	$\widehat{X} \xrightarrow{\widehat{X}} X$				
$X := f(X_1, X_2, \ldots, X_n)$	$X_i \xrightarrow{\hat{X}} X$,f.a. $1 \le i \le n$				
X:=U when C	$U \stackrel{\widehat{U} \cap [\mathcal{C}]}{\longrightarrow} X$				
X := U default V	$U \xrightarrow{\widehat{U}} X \xleftarrow{\widehat{V} \setminus \widehat{U}} V$				
$X := Y \$ n \text{ init } \overrightarrow{V}$	none				



Code generation

Generation for e.g. X := U default V given by

```
if present(X) then
    if present(U) then
        X := U
    end if
    if present(V) and not present(U) then
        X := V
    end if
end if
```



Equation systems





Equation systems





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Solving equation systems

Idea:

• Encode equation in a tree structure, called *clock trees*

General approach:

- Start with forest of clock trees
- Fusion of clock trees
- Repeat until inpossible



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- Extract synchronizations and data-dependency
- Solve clock equations
- Generate executable code



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